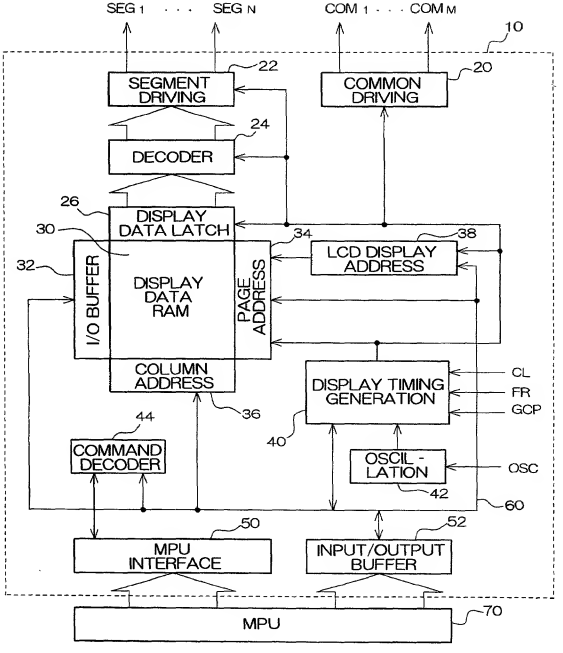
FIG. 1


SEG 1 · · · SEG N
COM 1 · · · COM M
10
SEGMENT DRIVING
22
COMMON DRIVING
20
DECODER
24
DISPLAY DATA LATCH
26
30
34
38
LCD DISPLAY ADDRESS
32
I/O BUFFER
DISPLAY DATA RAM
PAGE ADDRESS
COLUMN ADDRESS
36
DISPLAY TIMING GENERATION
CL
FR
GCP
40
44
COMMAND DECODER
OSCIL - LATION
OSC
42
60
50
MPU INTERFACE
52
INPUT/OUTPUT BUFFER
MPU
70

09912075 "0Z8Ih0T

FIG. 2

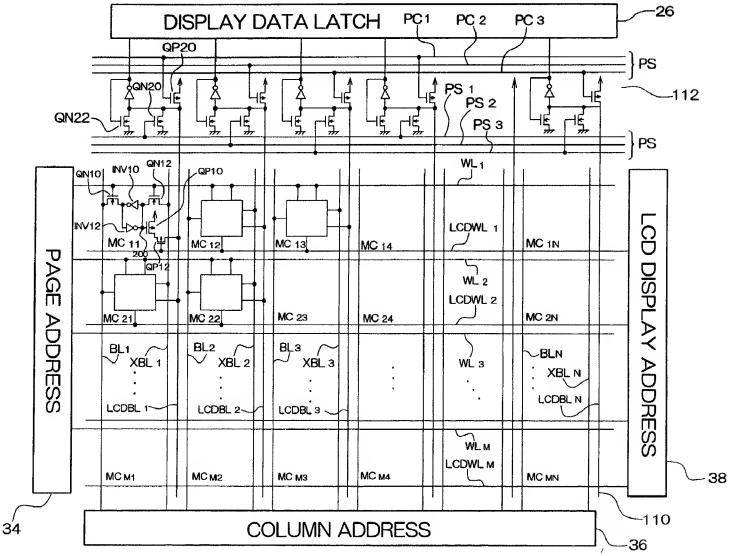


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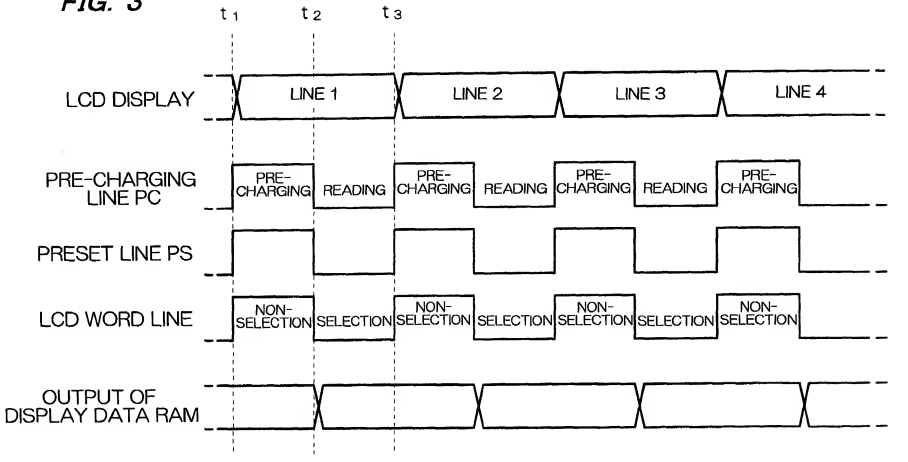
FIG. 3



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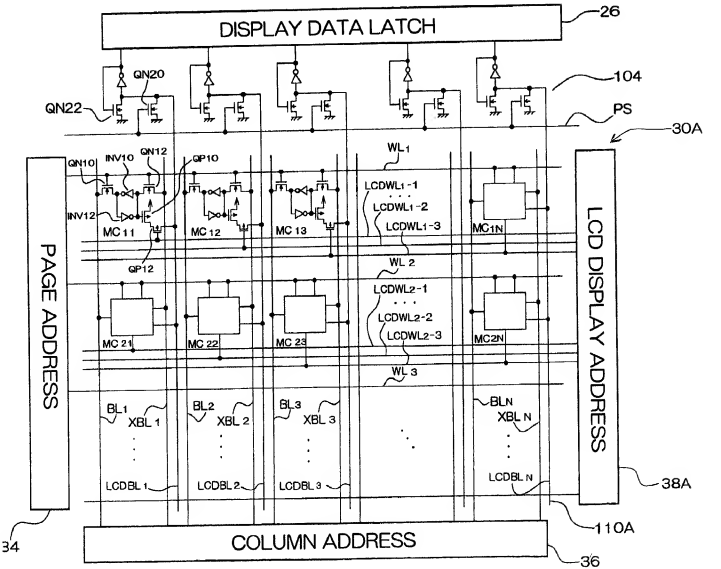
FIG. 4